Bosse Falgard
Prover Technology AB

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To whom it may concern

Letter of Support for an ARTES application

Founded in 1989, Prover Technology is a world leader in the industrial application of Formal Methods. Prover Technology develops and applies products for automatic validation and verification to ensure that complex systems are correct, free of faults, and function according to specification. To date our methods have been successfully applied to systems developed in avionics, nuclear power generation, railways, automotive and telecommunications, as well as a number of industrial and academic research projects at national and international level.

The basis for our commercial work is our implementation of Stålmarck's patented proof method. We are currently expanding the applicability of this algorithm to be able to model-check models of real-time systems. This work is partly carried out in the context of ESPRIT collaborations. We also regard PLC technology as a very important application area for our present and future commercial efforts. We have previously developed verification tools for PLC applications, and are currently involved in several projects where verification of PLCs is a central problem. We thus regard improved methods for verification and model-checking of PLC circuits as a very important problem, which has a big commercial potential. We are further convinced that Stålmarcks method is a most suitable basis for further development of such verification methods.

In conclusion, the project proposed by Parosh Abdulla, Uppsala University, addresses central problems with a big commercial potential for our company. We are definitely going to take part in the proposed project,

and expect to use the results in our commercial activities.

Yours Sincerely

Bosse Falgard